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## FIRST-YEAR OF MASTER OF SCIENCE PHYSICS REVISED SYLLABUS ACCORDING TO CBCS NEP2020

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**COURSE TITLE:-ADVANCED ELECTRONICS  
SEMESTER-II  
W.E.F. 2023-2024**

**RECOMMENDED BY THE BOARD OF STUDIES IN PHYSICS  
AND  
APPROVED BY THE ACADEMIC COUNCIL**

Devrukh Shikshan Prasarak Mandal's

Nya. Tatyasaheb Athalye Arts, Ved. S. R. Sapre Commerce, and  
Vid. Dadasaheb Pitre Science College (Autonomous), Devrukh.  
Tal.Sanameshwar, Dist. Ratnagiri-415804, Maharashtra, India

Academic Council Item No: **03 dated 8 July 2023**

Name of the Implementing Institute	:	Nya. Tatyasaheb Athalye Arts, Ved. S. R. Sapre Commerce, and Vid. Dadasaheb Pitre Science College (Autonomous), Devrukh. Tal. Sangmeshwar, Dist. Ratnagiri-415804,
Name of the Parent University	:	University of Mumbai
Name of the Programme	:	Master of Science
Name of the Department	:	Physics
Name of the Class	:	First Year
Semester	:	Second
No. of Credits	:	04
Title of the Course	:	Advanced Electronics
Course Code	:	S510PHT
Name of the Vertical in adherence to NEP 2020	:	Major
Eligibility for Admission	:	BSc in Physics
Passing Marks	:	40%
Mode of Assessment	:	Formative and Summative
Level	:	PG
Pattern of Marks Distribution for SEE and CIA	:	60:40
Status	:	NEP-CBCS
To be implemented from Academic Year	:	2023-2024

## Syllabus for First Year of Master of Science in Physics

(With effect from the academic year 2023-2024)

**SEMESTER-II**

**Paper No.– Physics Paper – I**

**Course Title: Advanced Electronics**

**No. of Credits - 04**

**Type of Vertical: Major**

**COURSE CODE: S510PHT**

**Learning Outcomes Based on BLOOM’s Taxonomy:**

After completing the course, the learner will be able to...

Course Learning Outcome No.	Blooms Taxonomy	Course Learning Outcome
CLO-01	Remember	Various formulae and steps involved in the methods
CLO-02	Understand	History of Microcontrollers and Microprocessors, Embedded versus External Memory Devices, 8-bit and 16-Microcontrollers, CISC and RISC Processors, Harvard and Von Neumann Architectures,
CLO-03	Apply	Apply various available methods to problems
CLO-04	Analyze	The basic concepts behind the mathematical methods
CLO-05	Evaluate	How to use a particular mathematical technique for solving the problem
CLO-06	Create	Select proper mathematical technique for a given problem

## Syllabus for First Year of Master of Science in Physics

(With effect from the academic year 2023-2024)

**SEMESTER - II**

**Paper No–Physics Paper–I**

**Course Title: Advanced Electronics**

**No. of Credits - 04**

**Type of Vertical: Major**

**COURSE CODE: S510PHT**

<b>COURSE CONTENT</b>			
<b>Module No.</b>	<b>Content</b>	<b>Credits</b>	<b>No. of Lectures</b>
<b>Unit 1</b>	<p><b>Microprocessors:</b> Counters and Time Delays, Stack and Sub-routines.</p> <p><b>Introduction to Microcontrollers:</b> Introduction, Microcontrollers and Microprocessors, History of Microcontrollers and Microprocessors, Embedded versus External Memory Devices, 8-bit and 16-bit Microcontrollers, CISC and RISC Processors, Harvard and Von Neumann Architectures, Commercial Microcontroller Devices.</p> <p><b>8051 Microcontrollers:</b> Introduction, MCS-51 Architecture, Registers in MCS-51, 8051 Pin Description, Connections, 8051 Parallel I/O Ports and Memory Organization.</p> <p><b>8051 Instruction set and Programming:</b> MCS-51 Addressing Modes and Instruction set. 8051 Instructions and Simple programs using Stack Pointer.</p> <p>Reference: - RSG, AVD</p>	<b>01</b>	<b>15</b>
<b>Unit 2</b>	<p><b>Power Supplies:</b> Linear Power supply, Switch Mode Power supply, Uninterrupted Power Supply, Step up and Step-down Switching Voltage Regulators.</p> <p><b>Inverters:</b> Principle of voltage driven inversion, Principle of current driven inversion, sine wave inverter, square wave inverter.</p> <p><b>Signal Conditioning:</b> Operational Amplifier, Instrumentation Amplifier using IC, Precision Rectifier, Voltage to Current Converter, Current to Voltage Converter, Op-Amp Based Butterworth Higher Order Active Filters and Multiple Feedback Filters, Voltage Controlled Oscillator, Analog Multiplexer, Sample and Hold circuits, Analog to Digital Converters, Digital to Analog Converters.</p> <p>Reference: - AJ, RAG, CD</p>	<b>01</b>	<b>15</b>
<b>Unit 3</b>	<p><b>Data Transmission Systems:</b> Analog and Digital Transmissions, Pulse Amplitude Modulation, Pulse Width Modulation, Time Division Multiplexing, Pulse Modulation, Digital Modulation, Pulse Code Format, Modems.</p>	<b>01</b>	<b>15</b>

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	<p><b>Optical Fiber:</b> Introduction to optical fibers, wave propagation and total internal reflection in optical fiber, structure of optical fiber, Types of optical fiber, numerical aperture, acceptance angle, single and multimode optical fibers, optical fiber materials and fabrication, attenuation, dispersion, splicing and fiber connectors, fiber optic communication system, fiber sensor, optical sources and optical detectors for optical fiber.</p> <p>Reference: - BS, HSK</p>		
<b>Unit 4</b>	<p><b>Instrumentation Circuits and Designs:</b> Microprocessors/ Microcontrollers based D C motor speed controller, Microprocessors/Microcontrollers based temperature controller, electronic weighing single pan balance using strain gauge/ load cell, Optical analog communication system using fiber link, electronic intensity meter using optical sensor, IR remote controlled ON/OFF switch.</p> <p>Reference: - AVD, MMM, KJA</p>	<b>01</b>	<b>15</b>
	<b>Total</b>	<b>04</b>	<b>60</b>

**Main references:**

- a. AVD: -Microcontrollers (Theory and Applications) by Ajay V. Deshmukh, TMH.
- b. RSG: - Microprocessor Architecture, Programming and Applications with the 8085 R. S. Gaonkar, 4th Edition. Penram International.
- c. RAG: - Op-Amps and Linear Integrated Circuits - R. A. Gayakwad , 3rd Edition Prentice Hall India.
- d. AJ: - Power Electronics and its applications, Alok Jain, 2nd Edition, Penram International India.
- e. CD: -Operational Amplifiers and Linear Integrated Circuits, Robert F. Coughlin and Frederic F. Driscoll, 6th Edition, Pearson Education Asia.
- f. BS: -A text book of optics; Brijlal Subramanyam.
- g. HSK: - Electronic Instrumentation, H.S. Kalsi, Tata-McGraw. Hill, 1999.
- h. MMM: - The 8051 Microcontroller & Embedded Systems by M.A. Mazidi, J.G. Mazidi and R.D. Mckinlay.
- i. KJA: - The 8051 Microcontroller: K.J. Ayala: Penram International.

**Additional References**

1. RK: - The 8051 Microcontroller and Embedded Systems, Dr. Rajiv Kapadia, Jaico Publishing House.
2. MP: - Programming & customizing the 8051 Microcontroller : Myke Predko, TMH.

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3. KG: - Optical Fiber Communications, Keiser G., Mcgraw Hill, Int. Learner Ed.
4. KD: - Electronic Communication Systems; 4th. Ed. Kennedy and Davis, (Tata-McGraw. Hill, 2004.

### **Access to the Course**

The course is available for all the students admitted for Master of Science in Physics.

### **Methods of Assessment**

The assessment pattern would be 60:40, 60% for Semester End Examination (SEE) and 40% for Continuous Internal Assessment (CIA). The structure of the SEE and CIA would be as recommended by the Board of Studies and approved by the Board of Examination and the Academic Council of the college.

### **Pattern of Evaluation**

The Examination/Evaluation pattern shall be framed by the Board of Examination with its final approval from the Academic Council of the College.